


# LCFC Confidential

## DY51 IO board Schematics Document

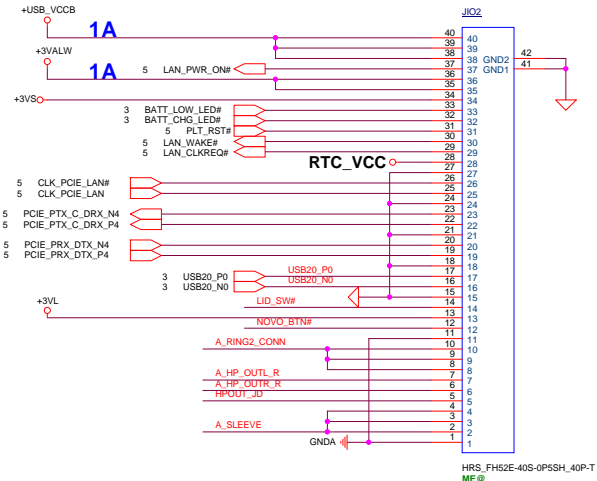
IO Board NS-B191

2016-11-25

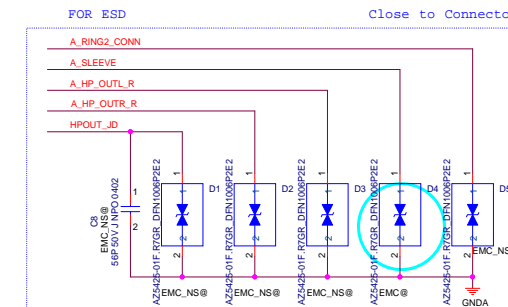
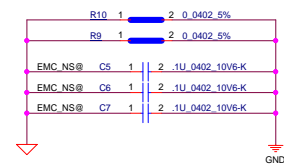
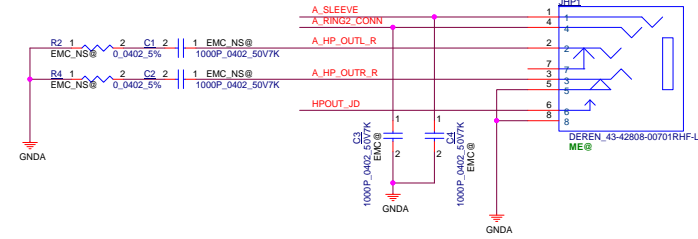
REV:1.0

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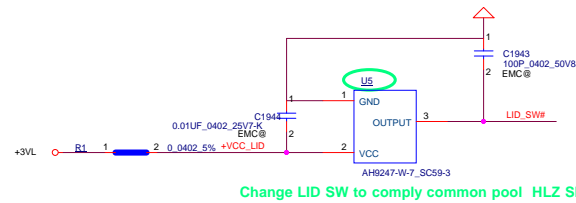
## USB I/O Connector



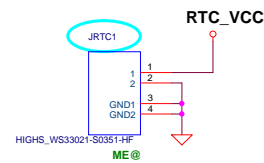
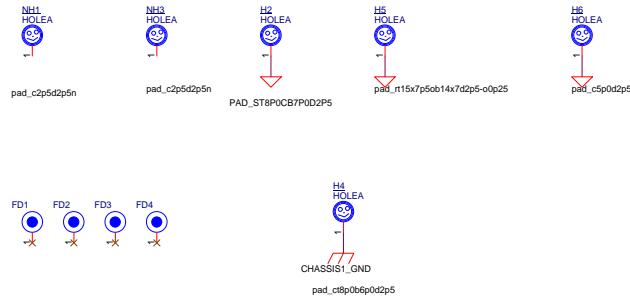
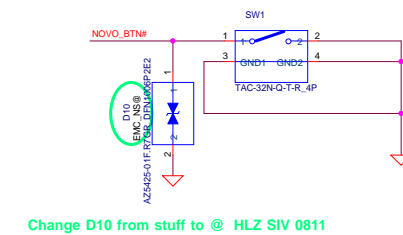
## HP/MIC Jack



## Hall Sensor

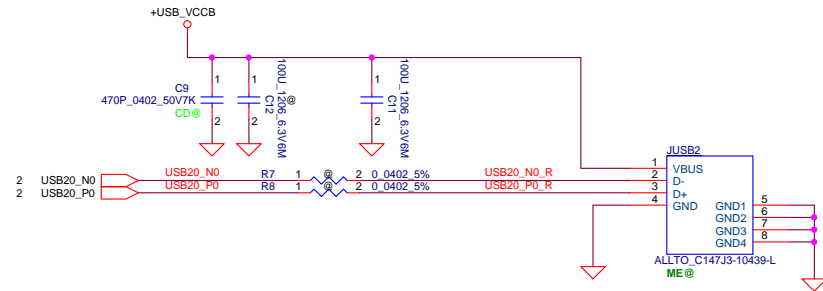


## NOVO Button

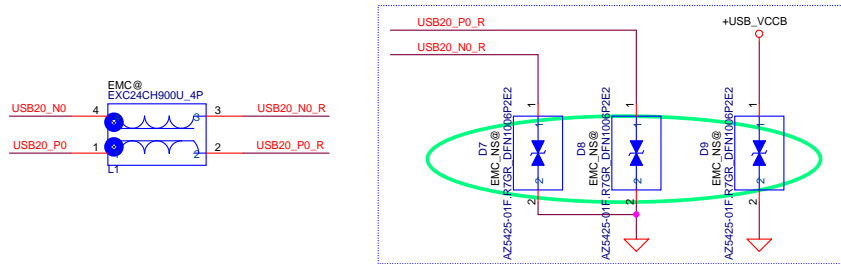


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## USB2.0 Port



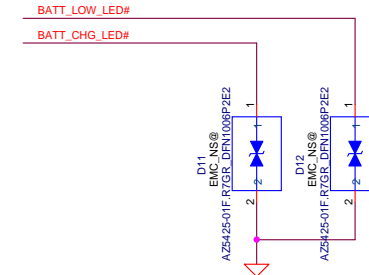
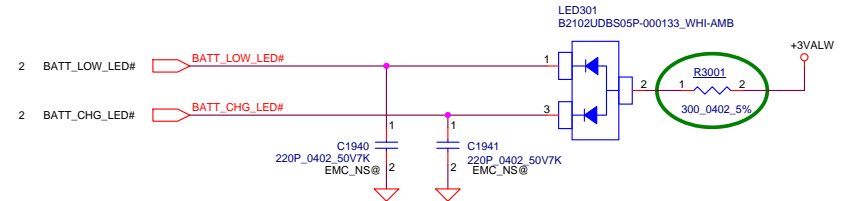
FOR ESD Close to Connector





Change D7&D8&D9 from stuff to @ HLZ SIV 0811

## Charger LED

Change R3001 from 1K to 300ohm due to Amber is weak Hai Y520 SVT

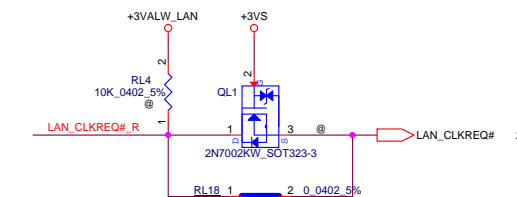


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				Issue	Issue, November 26, 2015	Issue	4 of 5



width : 40 mils

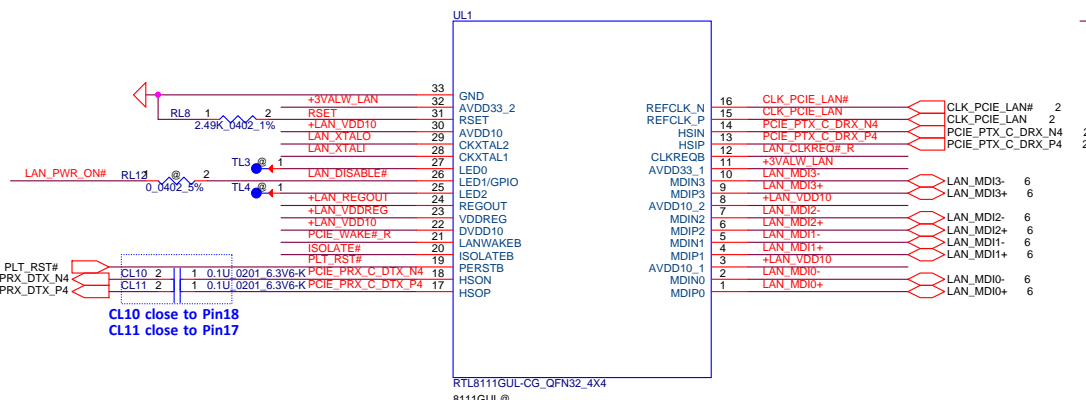
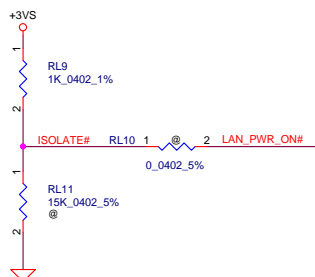


2 LAN\_WAKE#

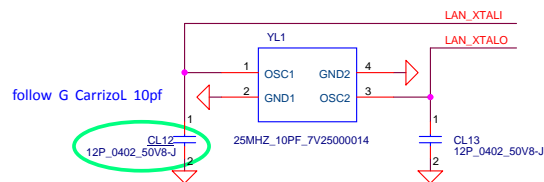
RL5 10K\_0402\_5% @

+3VALW\_LAN

1 PCIE\_WAKE#\_R



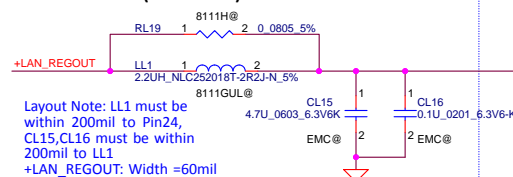
CL10 close to Pin18  
CL11 close to Pin17



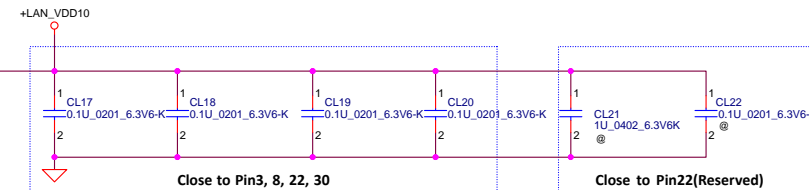
follow G CarrizoL 10pf

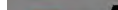
Change CL12 from 10P to 12PI HLZ SIV 0811

For RTL8111GUL/ RTL8106EUL (SWR mode)  
For RTL8111H (LDO mode) RL19 stuf f

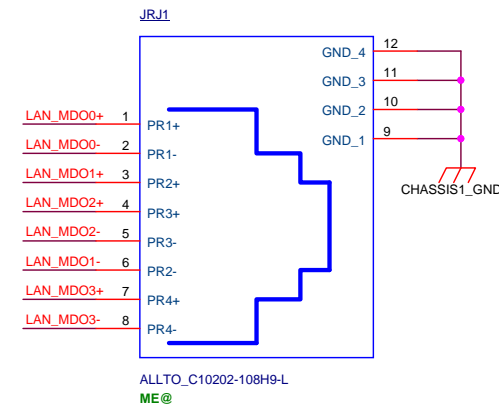
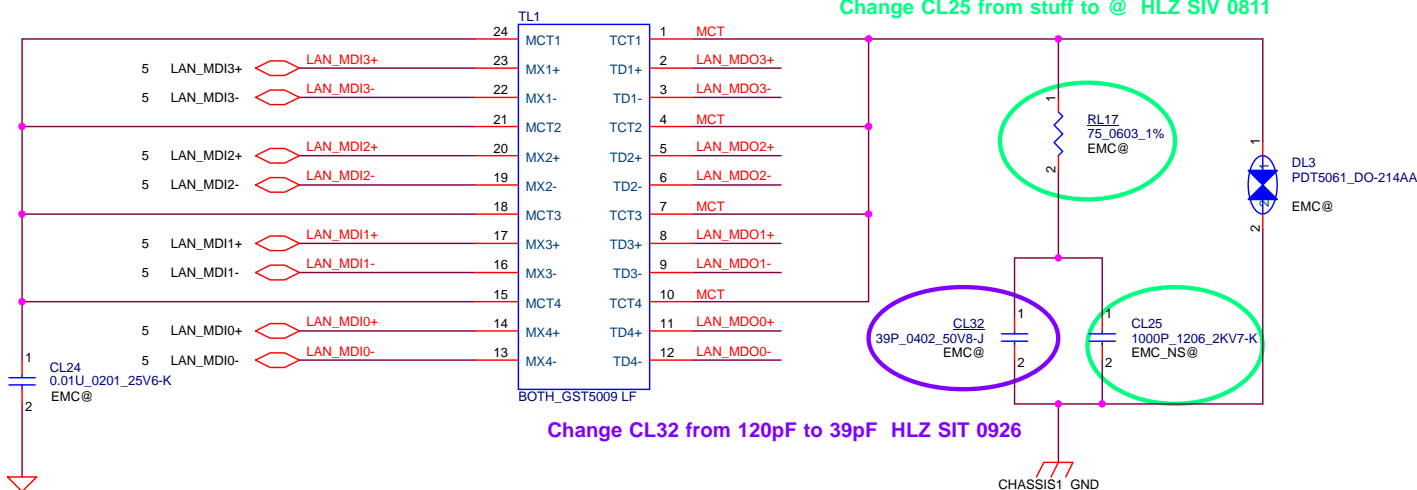
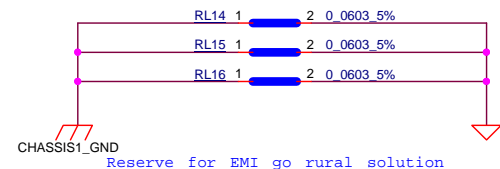
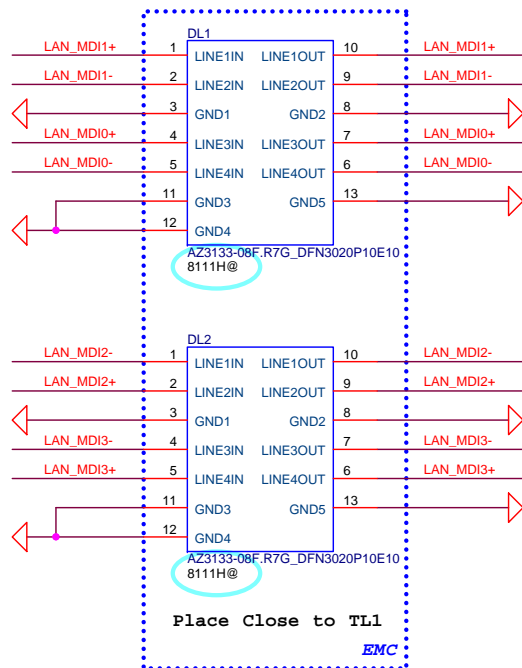


Layout Note: LL1 must be within 200mil to Pin24, CL15,CL16 must be within 200mil to LL1  
+LAN REGOUT: Width =60mil



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DL1/DL2  
1'S PN:SC300003M00



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